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(56) Documents Cited

EP 0456199 A2 EP 0178447 A2 WO 97/45827 A1  
JP 090260684 A US 5767003 A

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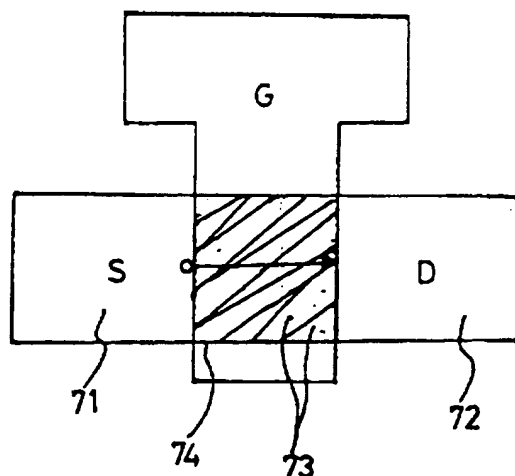
(54) Abstract Title

**Method of forming a thin film transistor by laser induced crystallisation in a predetermined direction**

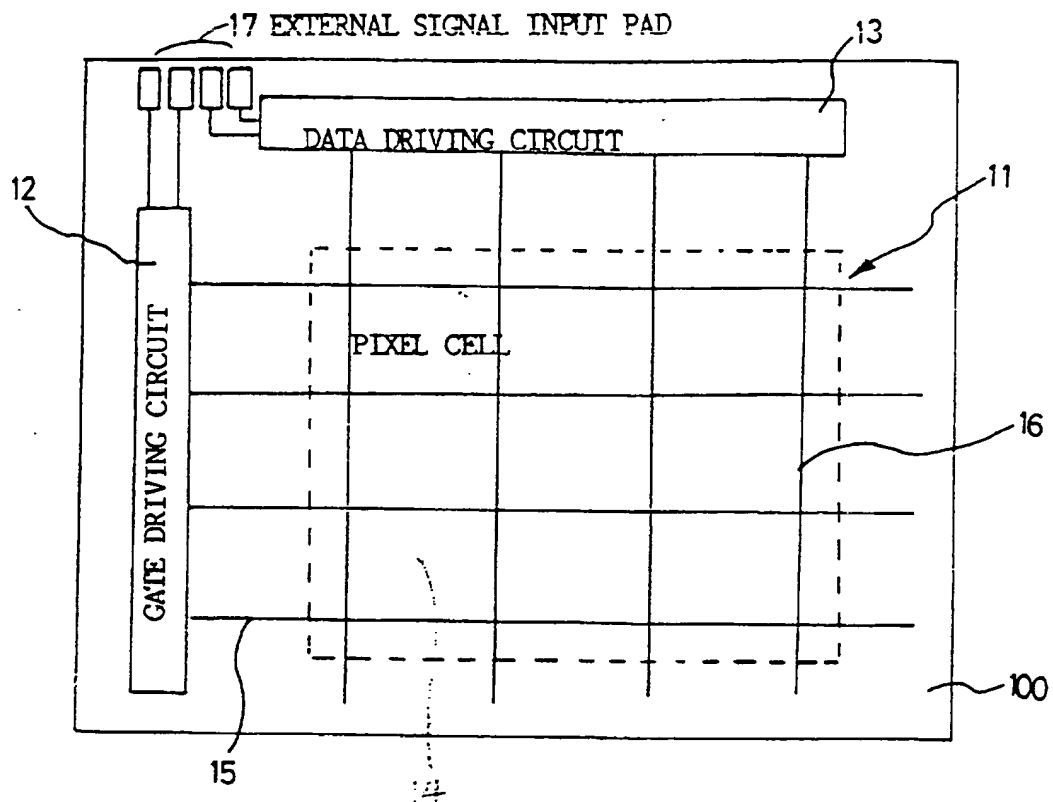
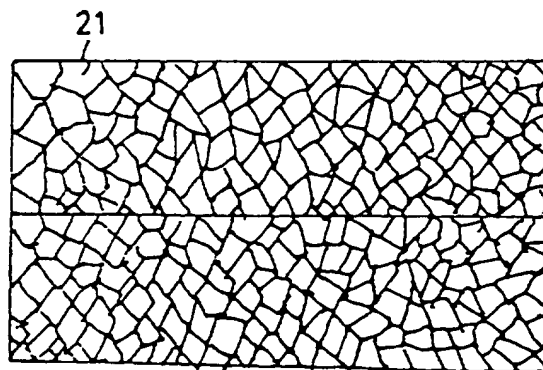
(57) A thin film transistor (TFT) for a liquid crystal display is formed on a polycrystallised silicon layer. An amorphous layer is crystallised by sequential lateral solidification, a technique in which a laser beam is scanned over the layer. Successive scans overlap so that the crystal grains 73 can grow to an increased size, and are oriented in a first direction.

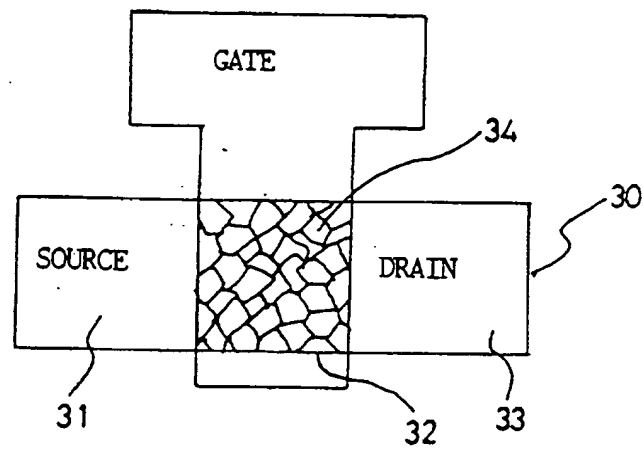
The layer is patterned to form an active layer which defines a channel region, with a channel direction inclined at an angle with respect to the crystal grain growth direction. A TFT array may be formed on the active layer.

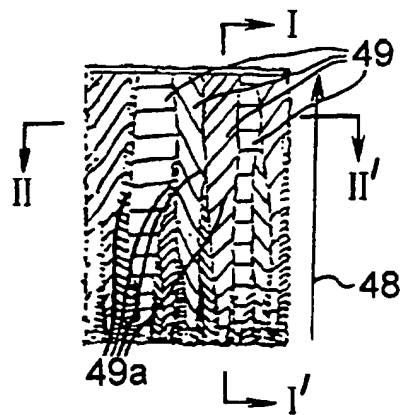
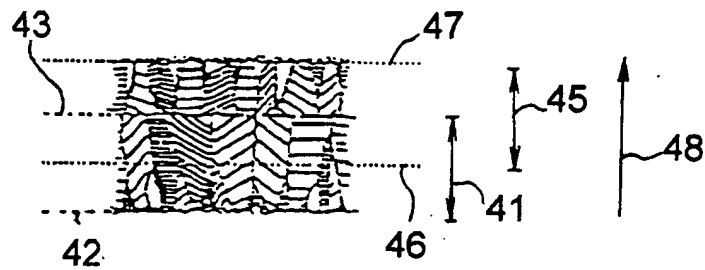
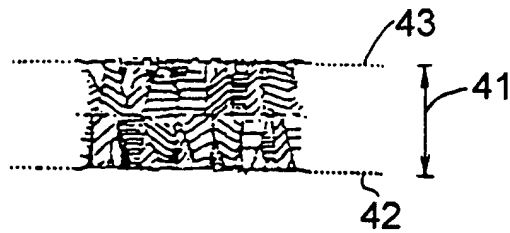
## FIG.7C

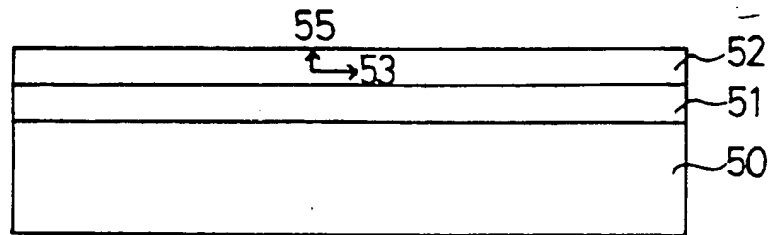
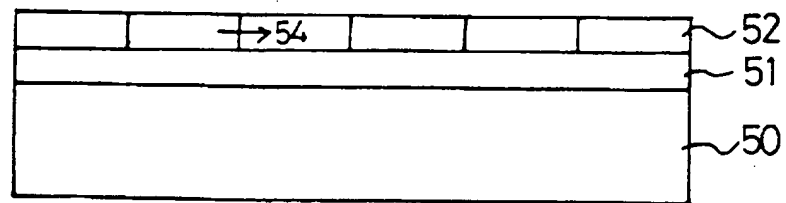


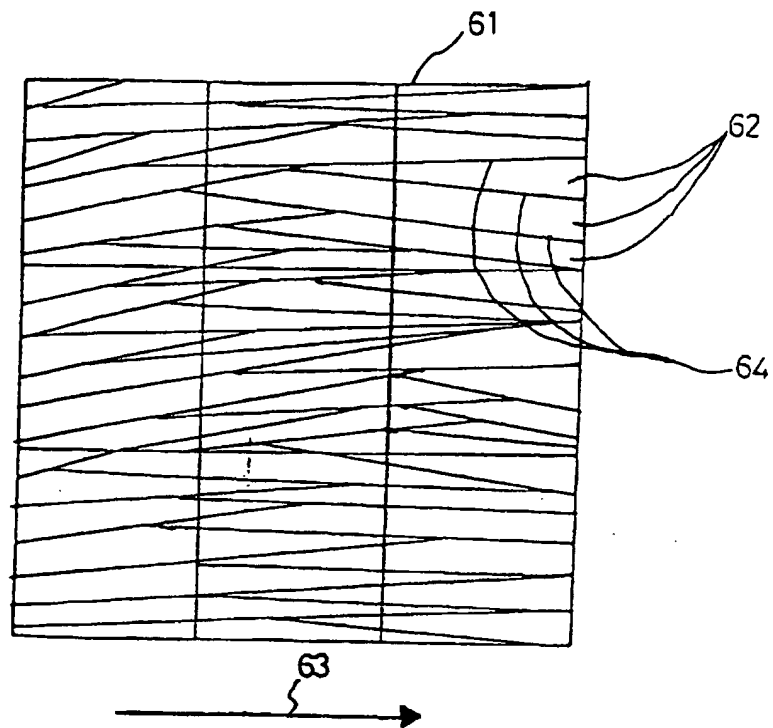
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**FIG.1****FIG.2 (PRIOR ART)**

**FIG.3**(PRIOR ART)



**FIG. 5A****FIG. 5B**

**FIG. 6**

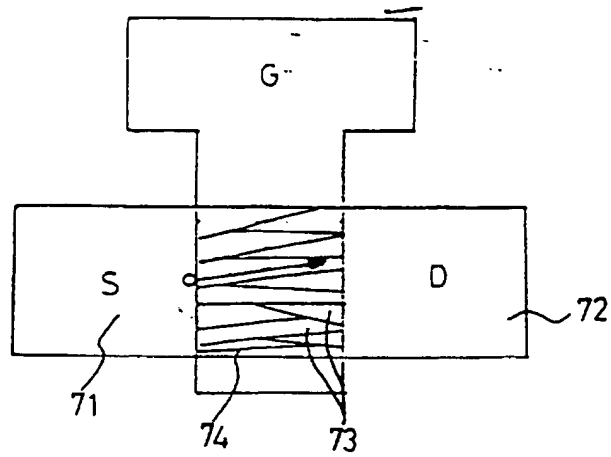
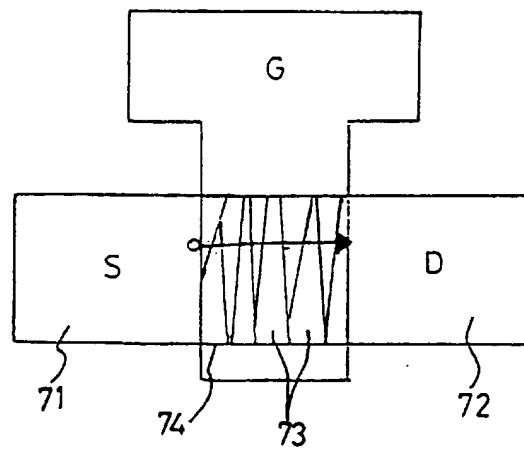
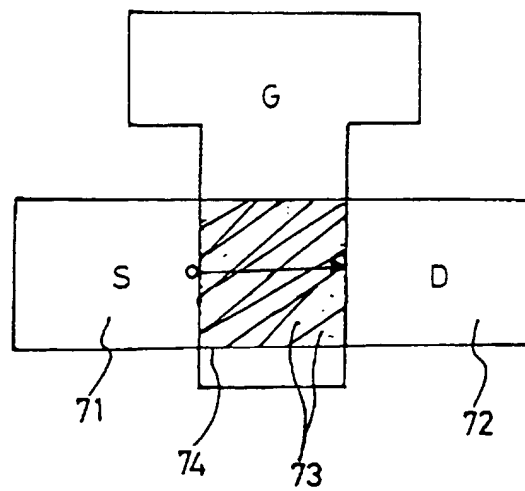
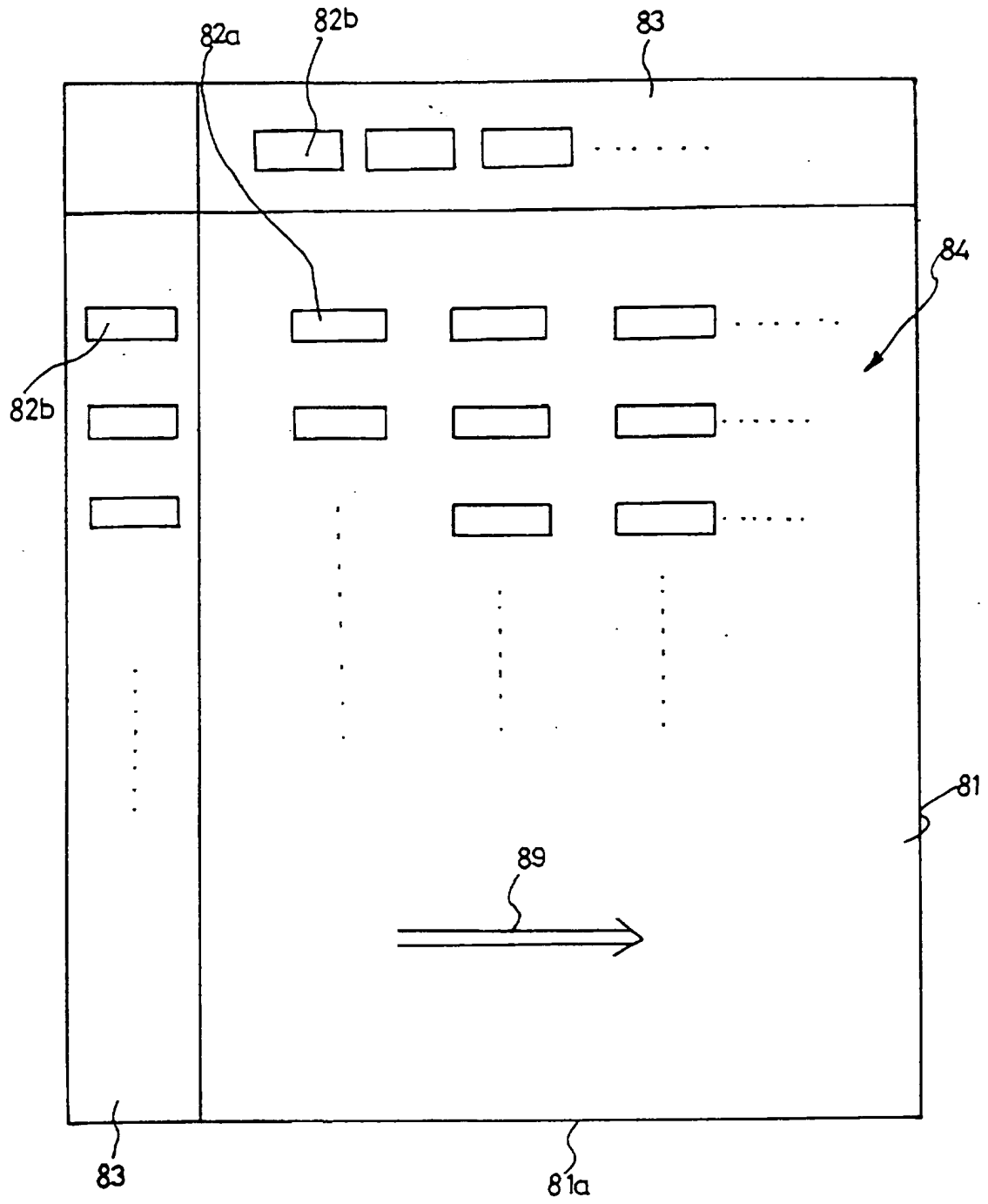
**FIG. 7A****FIG. 7B****FIG. 7C**

FIG. 8





METHOD OF FABRICATING THIN FILM TRANSISTORS  
FOR A LIQUID CRYSTAL DISPLAY

The present invention relates to a method of fabricating a thin film transistor for  
5 a liquid crystal display device.

A liquid crystal display (LCD) device is fabricated by forming various devices  
on a transparent insulated substrate, such as a glass substrate. To fabricate a thin film  
transistor (TFT) on a weak heat-resistant or heat-sensitive substrate, such as a glass  
10 substrate, an amorphous silicon layer or a polycrystalline silicon layer is first formed on  
the substrate. An active layer for the TFT is then formed by etching the silicon layer.  
Characteristics of charge carrier mobility are excellent when a polycrystalline silicon  
layer is formed on the glass substrate. Accordingly, when a polycrystalline silicon layer  
is used, devices for a driver and a pixel array of the LCD device are fabricated on the  
15 same substrate simultaneously.

Fig. 1 is a schematic drawing of an LCD device on which a pixel array and a  
driver circuit are simultaneously formed on the same substrate. The pixel array 11 lies  
in the middle of the substrate 100, and a gate driver 12 and a data driver 13 are located  
near two sides of the pixel array 11 on the substrate 100. The pixel array 11 comprises  
20 a plurality of pixel cells 14 defined by a plurality of gate lines 15 connected to the gate  
driver 12 and a plurality of data lines 16 connected to the data driver 13. The gate  
driver 12 drives the pixel cells. The data driver 13 supplies the cells with data signals.  
The gate driver 12 and the data driver 13 receive external signals through external  
signal input terminals 17 and supply them to pixel cells 14. The drivers use inverters of

complementary TFTs to generate proper signals for the pixel cells 14.

Fig. 2 is a schematic drawing of silicon grains 21 in a polycrystalline silicon layer which is formed according to a related art. According to Fig. 2, an amorphous silicon layer is formed on a substrate. A laser beam is applied to the amorphous silicon layer and moves by a predetermined distance for each laser pulse. Accordingly, the substrate is scanned by the laser beam, during which the portion of the silicon layer irradiated with the laser beam is melted and crystallized by solidification. By controlling laser energy properly, the portion of the silicon irradiated with the laser beam is almost completely melted, with an unmelted portion remaining at an interface between the silicon layer and the substrate.

In the portion of the silicon layer irradiated with the laser beam, silicon grains grow laterally, rather than in the direction of the thickness of the layer, by using other portions of the silicon that remain unmelted as seeds. Depending on a state of the amorphous silicon layer and the laser energy supply, the seeds are located at an interface between the substrate. Therefore, a polycrystalline silicon layer having grains of irregular sizes at random locations is formed.

When fabricating a TFT of a coplanar type, an active layer is formed by patterning a polycrystalline layer formed on the insulated substrate using, e.g., photolithography. A gate insulating layer and a gate electrode are then formed on the active layer, and a source and a drain region are formed by doping the active layer with impurities to form a TFT for the LCD device.

Fig. 3 shows a schematic layout of a polycrystalline silicon TFT according to the related art. The grain pattern of the active layer has no significance on the channel direction because the silicon grains in the active layer are scattered irregularly on the

substrate. Referring to Fig. 3, a plurality of silicon grains 34 are included in an active layer 30 of the TFT. The charges moving from the source region 31 through the channel region 32 toward the drain region 33 of the TFT are greatly affected by the grain boundaries in the channel region. Hence, the charge carrier mobility of the polycrystalline silicon TFT is much smaller than that of a single crystalline silicon TFT.  
5 Moreover, physical characteristics of the TFT formed on the substrate are irregular since the polycrystalline silicon layer contains irregular grains.

Accordingly, circuit malfunctioning may occur in the gate and data drivers which include TFTs, causing external signals to be transferred unevenly to the gate and data lines. In addition, the TFTs formed irregularly in the pixel array may cause the  
10 image characteristics of the LCD to deteriorate.

Accordingly, the present invention is directed to a method of fabricating thin film transistors for a liquid crystal display device that substantially obviates one or  
15 more of the problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a method of fabricating TFTs for an LCD by which physical characteristics of TFTs on a substrate become uniform.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be  
20 learned by practicing the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve these and other advantages and in accordance with the purposes of the present invention, as embodied and broadly described, a method of fabricating a

thin film transistor (TFT) for a liquid crystal display device having a driver and a pixel array formed on a substrate comprises the steps of forming a polycrystalline silicon layer on the substrate by growing silicon grains in a first direction from an amorphous silicon layer using a sequential lateral solidification technique; forming an active layer  
5 by patterning said polycrystalline silicon layer, said active layer defining a channel of the TFT having a direction inclined at a predetermined angle with respect to said first direction; and forming the TFT on the active layer.

According to another embodiment of the present invention, a method of fabricating a liquid crystal display (LCD) device having a driver and a pixel array  
10 formed on a substrate comprises the steps of forming a polycrystalline silicon layer on the substrate by growing silicon grains in a first direction from an amorphous silicon layer using a sequential lateral solidification technique; forming an active layer by patterning said polycrystalline silicon layer, said active layer defining a plurality of channel regions having a second direction inclined at a predetermined angle with  
15 respect to said first direction; and forming a plurality of thin film transistors (TFTs) on the active layer, the channel regions of the active layer forming the channels of the TFTs, wherein the TFTs are devices for the driver and the pixel array of the LCD device.

In another aspect of the present invention, a liquid crystal display (LCD) device  
20 comprises a gate drive circuit having a plurality of thin film transistors (TFTs); a data drive circuit having a plurality of TFTs; a plurality of gate lines and data lines connected to the gate and data drive circuits, respectively; and a pixel array having a plurality of pixel cells defined by the gate lines and data lines, each pixel cell having a TFT, wherein each TFT has a channel region formed of a thin polycrystalline silicon

layer by a sequential lateral solidification technique, the polycrystalline silicon layer having elongated silicon grains grown laterally in a predetermined direction and having substantially no grain boundaries in the direction of the thickness of the layer, and wherein the TFTs define a channel direction which is inclined at a predetermined angle  
5 with respect to the growth direction of the silicon grains.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

10 Specific embodiments according to the invention will now be described, by way of example, with reference to the accompanying drawings, in which:

Fig. 1 is a schematic layout of an LCD device having a driver formed integrally on the device;

Fig. 2 schematically illustrates the state of the silicon grains in a polycrystalline  
15 silicon TFT according to the related art;

Fig. 3 illustrates a layout of a TFT having a polycrystalline silicon layer shown in Fig. 2;

Figs. 4A to 4C illustrate a method for crystallizing an amorphous silicon film using the SLS technique;

20 Fig. 5A and Fig. 5B are cross-sectional views of a silicon layer of Fig. 4C along the lines I-I' and II-II', respectively;

Fig. 6 shows a state of the silicon grains of a polycrystalline silicon layer formed by sequential lateral solidification;

Figs. 7A - 7C show TFTs having a polycrystalline silicon layer as that shown in

Fig. 6; and

Fig. 8 shows a pattern of active layers of the TFTs on an entire substrate.

According to the present invention, an active layer of the TFT is fabricated by  
5 patterning a polycrystalline silicon layer which is formed by growing silicon grains  
using the technique of sequential lateral solidification. A method according to the  
present invention for forming an LCD device having a driver and a pixel array formed  
on the same substrate includes the steps of forming a polycrystalline silicon layer by  
growing silicon grains in a first direction from an amorphous silicon layer using the  
10 technique of sequential lateral solidification, forming an active layer by patterning the  
polycrystalline silicon layer, wherein the active layer defines a channel direction  
oriented at a predetermined angle with respect to the first direction, and forming the  
gate, source and drain electrodes of the thin film transistor. Uniformity of the devices  
are ensured when the predetermined angle is 0 degree or between 30 and 60 degrees  
15 and when the channel directions of the TFTs are the same throughout the entire  
substrate.

The preferred embodiments of the present invention will now be described in  
detail, examples of which are illustrated in the accompanying drawings.

Figs 4A to 4C illustrate a method for crystallizing an amorphous silicon film  
20 using the SLS technique. A technique of forming a single crystalline silicon layer on a  
glass substrate by SLS is described in Robert S. Sposilli, M. A. Crowder, and James S.  
Im, Mat. Res. Soc. Symp. Proc. Vol. 452, 956-957, 1997. The technique, using the fact  
that silicon grains tend to grow vertically against the interface between liquid and solid  
silicon, teaches that an amorphous silicon layer is crystallized by controlling the

magnitude of laser energy and an irradiation range of a moving laser beam to form silicon grains grow laterally up to a predetermined length.

SLS is one technique that forms the silicon layer, by applying laser to an amorphous silicon layer. Here, the displacement of the laser beam is shorter than the  
5 length of lateral growth of the crystal. Consequently, silicon particles which are longer than 10 are formed on a glass substrate. The foregoing technique is disclosed in "Crystalline Si Films For Integrated Active Matrix Liquid-Crystal Displays," MRS Bulletin, Volume XXI, Number 3, March 1996, pp. 39~48.

Moreover, the location, size, and shape of a crystal particle may be controlled by  
10 manipulating the shape of a slit through which a laser beam passes, which enables the formation of silicon particles that are larger than an active area of a TFT. Thus, it is possible to fabricate a TFT of single crystalline silicon manufactured by forming an active area of the TFT as a single crystal particle.

Referring to Fig. 4A, a narrow region 41 of the film having the shape of a  
15 straight slit bounded by dashed lines 42 and 43 is irradiated with a laser beam at an energy density sufficient to induce complete melting of the silicon. Subsequently, lateral grain growth proceeds from the unmelted regions adjacent the narrow strip region 41 which is fully melted. The grain boundaries in such directionally solidified materials tend to form in a direction substantially perpendicular to the interface between  
20 a unmelted and the melted regions. Depending on the width of the molten region, lateral growth cease when either one of the following occur, whichever is first: (1) the two opposing growth fronts collide at the center, or (2) the melted region becomes sufficiently supercooled so that bulk nucleation of solids occur. Because of these factors, the maximum lateral growth distance that can be achieved with a single laser  
25 pulse is limited to a certain length, which may be referred to as the single-pulse lateral

growth distance. The single-pulse lateral growth distance depends on the film thickness and the incident laser energy density.

Referring to Fig. 4B, the laser beam image is translated relative to the film in the direction indicated by the arrow 48 over a distance less than the single-pulse lateral growth distance. A narrow region 45 bound by dashed lines 46 and 47 is irradiated by a second laser pulse. After the second laser pulse, lateral grain growth recommences from the edges of the completely melted region 45. Since one of these edges, in this case the edge 46, is located within the silicon region grown during the previous irradiation step, the lengths of the silicon grains formed by the previous irradiation are extended by the second irradiation beyond the single-pulse lateral growth distance.

Referring to Fig. 4C, the above-cited irradiating and solidifying processes are repeated as many times as necessary to grow silicon grains of any desired length. The final microstructure of the silicon layer obtained in this fashion is shown in Fig. 4C, showing grains 49 extending in the direction of the laser beam scan 48, as well as grain boundaries 49a. The lengths of the silicon grains formed by the above SLS technique is remarkably large.

Figs. 5A and 5B show the cross-sectional drawings along the lines I-I and II-II of Fig. 4C. Here, a silicon layer 52, formed by SLS crystallization technique is superimposed on an insulating layer 51, which is in turn, located on an insulating substrate 50.

According to the SLS technique used herein, the silicon grains proceed by means of lateral grain growth. Thus, the grain boundaries in directionally solidified materials tend to form perpendicularly to the melt interface. A single pulse grain growth is larger than the thickness of the amorphous silicon film.

Therefore, the SLS silicon thin film 52, as shown in Fig. 5A, has no boundary in both a first direction 53, i.e., the crystallizing direction (in Fig. 4C), and a second



direction 55 which is perpendicular to the plane of the substrate. There is only one silicon grain shown in the silicon thin film 52 in Fig. 5A, which is a cross-sectional drawing along the line I-I in Fig. 4C.

However, the SLS silicon thin film 52 has a plurality of boundaries in a third  
 5 direction 54, i.e., the perpendicular direction to the first direction 53, the crystallizing direction.

Therefore, a plurality of silicon grains may be shown in the silicon thin film 52 in Fig. 5B, which is a cross-sectional drawing along the line II-II in Fig. 4C.

Fig. 6 is a top view showing the whole surface of the SLS silicon layer. The  
 10 SLS silicon layer 61 is formed using the SLS technique by growing silicon grains 62 in the direction of the laser beam scan 63. A plurality of grain boundaries 64 also extend in the same direction as the direction of the laser beam scan 63.

To fabricate a TFT of a coplanar type using a method according to the present invention, an active layer is formed by patterning the polycrystalline silicon layer  
 15 formed on an insulated substrate. A gate insulating layer and a gate electrode are then formed, and a source and a drain region are formed by doping the active layer with impurities to form the TFT.

Figs. 7A - 7C show layouts of various examples of TFTs having polycrystalline silicon layers crystallized by using SLS and the patterning method according to the  
 20 present invention. Three examples are shown, where the channel direction (defined by the direction that points from the source region 71 to the drain region 72) are at different angles with respect to the growth direction of the grains 73.

Fig. 7A shows a first TFT having a channel direction which is inclined 0 degree with respect to the growth direction of silicon grains 73 in the channel region 74.

25 Referring to Fig. 7A, the channel direction lies in parallel with the growth direction of silicon grains 73. There are minimum grain boundaries to inhibit the

mobility of electric charge carriers in the channel region. Accordingly, the TFT having the construction of Fig. 7A shows the same or comparable physical characteristics as a single crystalline silicon TFT.

Fig. 7B shows a second TFT having a channel direction which is inclined 90  
5 degrees with respect to the growth direction of the silicon grains 73 in the channel region 74. Referring to Fig. 7B, a channel direction is formed to be perpendicular to a growth direction of silicon grains. The mobility of electric charge carriers is low as charge carriers moving through the channel region are inhibited by a plurality of grain boundaries. Accordingly, the TFT having the construction of Fig. 7B shows the same  
10 or comparable physical characteristics as a TFT formed of polycrystalline silicon having small grains.

Fig. 7C shows a third TFT having a channel direction which is inclined 45 degrees with respect to the growth direction of silicon grains 73. While 45 degrees is illustrated in this example, any angle between 0 and 90 degrees may be used. In this  
15 case, electric charge carriers moving through the channel region have to pass through a plurality of grain boundaries. The charge carrier mobility is good since the number of silicon grains in the channel region is smaller than that of the related art. The characteristics of the TFTs are uniform throughout the entire LCD device when the channel direction has an inclination between 30 to 60 degrees with respect to the growth  
20 direction of the silicon grains. This is because the active layer in the channel regions of all the TFTs have approximately equal numbers of grain boundaries. A TFT having the above construction has physical characteristics and circuit performance better than those of a polycrystalline silicon TFT.

It is important for an LCD device to have semiconductor devices having

uniform characteristics in the driver and the pixel array. Image characteristics of the LCD improves significantly when signals for the gate and data lines are transferred to all pixels uniformly and when the pixels have uniform physical characteristics.

When an LCD device using a polycrystalline silicon layer having grains grown  
5 laterally by the SLS method is to be fabricated, the TFTs should be formed such that the physical characteristics of the TFTs in the drive circuit and the pixel array are uniform. For this purpose, the first TFT shown in Fig. 7A which has the same characteristics as a single crystalline silicon TFT may be used for the driver and/or the pixel array.

Moreover, the third TFT shown in Fig. 7C which has an excellent electric  
10 charge carrier mobility and the same number of grain boundaries among the TFTs may be used for the driver and/or the pixel array.

Fig. 8 illustrate patterns of an active layer for the first type of TFTs shown in Fig. 7A which ensure uniformity among the TFTs across the entire substrate 81. The active layer is a polycrystalline silicon layer of silicon grains grown a first direction  
15 by crystallizing an amorphous silicon layer using SLS.

Each block 82a or 82b indicates an area of the active layer accommodating the channel region of a TFT. In the example illustrated in Fig. 8, the TFTs 82a in the pixel array 84 and the TFTs in the data and gate drive circuits 83 have channel regions that are in the same direction and are parallel to the crystallizing direction 89 by SLS.

20 As pointed out above, when TFTs are formed in an SLS silicon layer, it is important to form and arrange patterns of active layers where the channel directions are determined by the growth direction of the silicon grains. Uniformity of devices through the whole substrate is obtained by forming the TFTs so that their channels are oriented in the same direction.

In addition, TFTs having excellent physical characteristics are formed uniformly on the entire substrate by forming the TFTs so that their channel directions are inclined at a predetermined angle with respect to the growth direction of the silicon grains.

It will be apparent to those skilled in the art that various modifications and  
5 variations can be made in the method of fabricating a thin film transistor for a liquid crystal display of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention covers the modifications and variations of the illustrated embodiments provided they come within the scope of the appended claims and equivalents.

CLAIMS:

1. A method of fabricating a thin film transistor (TFT) for a liquid crystal display device having a driver and a pixel array formed on a substrate, said method comprising:

forming a polycrystalline silicon layer on the substrate by growing silicon grains in a first direction from an amorphous silicon layer using a sequential lateral solidification technique;

forming an active layer by patterning said polycrystalline silicon layer, said active layer defining a channel of the TFT having a second direction; and

forming the TFT on the active layer.

2. The method according to claim 1, wherein said first direction is inclined to said second direction by between about 30 and 60 degrees.

3. The method according to claim 1, wherein said first direction is inclined to said second direction by about 0 degrees.

4. The method according to any of claims 1 to 3, wherein said TFT is a device for the driver.

5. A method of fabricating a liquid crystal display (LCD) device having a driver and a pixel array formed on a substrate, comprising:

forming a polycrystalline silicon layer on the substrate by growing silicon grains in a first direction from an amorphous silicon layer using a sequential lateral solidification technique;

forming an active layer by patterning said polycrystalline silicon layer, said active layer defining a plurality of channel regions having a second direction; and

forming a plurality of thin film transistors (TFTs) on the active layer, the channel regions of the active layer forming the channels of the TFTs, wherein the TFTs are devices for the driver and the pixel array of the LCD device.

6. The method according to claim 5, wherein said first direction is inclined to said second direction by between about 30 and 60 degrees.

7. The method according to claim 5, wherein said first direction is inclined to said second direction by about 0 degrees.

8. A liquid crystal display (LCD) device comprising:  
a gate drive circuit having a plurality of thin film transistors (TFTs);  
a data drive circuit having a plurality of TFTs;  
a plurality of gate lines and data lines connected to the gate and data drive circuits, respectively; and  
a pixel array having a plurality of pixel cells defined by the gate lines and data lines, each pixel cell having a TFT,

wherein each TFT has a channel region formed of a thin polycrystalline silicon layer by a sequential lateral solidification technique, the polycrystalline silicon layer having elongated silicon grains grown laterally in a predetermined growth direction and having substantially no grain boundaries in the direction of the thickness of the layer, and wherein the TFTs define a channel direction.

9. The LCD device according to claim 8, wherein said growth direction is inclined to said channel direction by between about 30 and 60 degrees.

10. The LCD device according to claim 8, wherein said growth direction is inclined to said channel direction by about 0 degrees.

11. The LCD device according to any of claims 8 to 10, wherein channel

regions of the TFTs of the gate driver circuit have substantially the same direction as channel regions of the TFTs of the data driver circuit.

12. The LCD device according to any of claims 8 to 10, wherein channel regions of the TFTs of the gate driver circuit have substantially the same direction as the channel region of the TFT of each pixel cell.

13. The LCD device according to any of claims 8 to 10, wherein channel regions of the TFTs of the data driver circuit have substantially the same direction as the channel region of the TFT of each pixel cell.

14. The LCD device according to any of claims 8 to 10, wherein channel regions of the TFTs of the gate and data driver circuits have substantially the same direction as the channel region of the TFT of each pixel cell.

15. A method of fabricating a thin film transistor substantially as described herein with reference to and/or substantially as illustrated in Figs. 1 and 4A to 8 of the accompanying drawings.

16. A method of fabricating a liquid crystal display device substantially as described herein with reference to and/or substantially as illustrated in Figs. 1 and 4A to 8 of the accompanying drawings.

17. A liquid crystal display device substantially as described herein with reference to and/or substantially as illustrated in Figs. 1 and 4A to 8 of the accompanying drawings.



Application No: GB 9913339.9  
Claims searched: All

Examiner: Matthew Lincoln  
Date of search: 29 September 1999

**Patents Act 1977**  
**Search Report under Section 17**

**Databases searched:**

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.Q): H1K (KLHA, KLXW)

Int Cl (Ed.6): H01L 21/20

Other: Online: EPODOC, JAPIO, WPI

**Documents considered to be relevant:**

Category	Identity of document and relevant passage	Relevant to claims
X	EP 0456199 A2 (ASAHI GLASS) Whole document	1, 4, 5, 8, 11 to 14
X	EP 0178447 A2 (FUJITSU) Page 9 line 18 to page 10 line 24, in particular	1, 3 to 5, 7, 8, 10 to 14
X	WO 97/45827 A1 (UNIVERSITY OF COLUMBIA) Third embodiment and figure 3B in particular	1, 3 to 5, 7, 8, 10 to 14
X	JP 090260684 (TOSHIBA) Please see English language equivalent US 5858807, whole document.	1, 4, 5, 8, 11 to 14
X	US 5767003 (NOGUCHI) Column 5 line 51 to column 6 line 13	1, 4, 5, 8, 11 to 14

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